

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1 Claim 1 (currently amended): A receiver, comprising:

2 a tuner for selecting a desired broadcasting station from among a plurality of broadcasting
3 stations and receiving a digital broadcast of the selected station, the ~~for receiving a digital broadcast~~
4 having a transmission spectrum format which is prepared by dividing digital data comprising a single
5 bit stream into a plurality of bit frames capable of holding the same contents as the original bit
6 stream, allocating different carrier frequencies to the respective bit frames and arranging the bit
7 frames on a frequency axis, ~~the digital broadcast receiver comprising a tuner for selecting the desired~~
8 ~~broadcasting station from among a plurality of broadcasting stations and receiving the digital~~
9 ~~broadcast of the selected station;~~

10 a demodulator circuit for decoding ~~[[a]]~~ the plurality of bit frames constituting the received
11 digital broadcast and outputting the decoded bit frames; and

12 a control circuit for controlling the operation of the tuner and the demodulator circuit, ~~the~~
13 ~~digital broadcast receiver being characterized in that~~ the control circuit comprising:

14 decoding control means for stopping decoding at least one of the bit frames, with one
15 broadcasting station selected,

16 search means for searching for other broadcasting station or stations of satisfactory reception

17 condition utilizing free time resulting from stopping decoding, and

18 selected station changeover means for changing over the broadcasting station being selected
19 for reception to the broadcasting station of satisfactory reception condition searched for.

1 Claim 2 (currently amended): A receiver for receiving a digital broadcast having a
2 transmission spectrum format which is prepared by dividing digital data comprising a single bit
3 stream into a plurality of bit frames capable of holding the same contents as the original bit stream,
4 allocating different carrier frequencies to the respective bit frames and arranging the bit frames on
5 a frequency axis, the digital broadcast receiver comprising a tuner for selecting the desired
6 broadcasting station from among a plurality of broadcasting stations and receiving the digital
7 broadcast of the selected station, a demodulator circuit for decoding a plurality of bit frames
8 constituting the received digital broadcast and outputting the decoded bit frames and a control circuit
9 for controlling the operation of the tuner and the demodulator circuit, the digital broadcast receiver
10 being characterized in that the control circuit comprising:

11 decoding control means for stopping decoding at least one of the bit frames, with one
12 broadcasting station selected,

13 search means for searching for other broadcasting station or stations of satisfactory reception
14 condition utilizing free time resulting from stopping decoding, and

15 selected station changeover means for changing over the broadcasting station being selected
16 for reception to the broadcasting station of satisfactory reception condition searched for, A digital

17 ~~broadcast receiver according to claim 1~~ wherein the control circuit further comprises a sensor means
18 for detecting an impaired reception condition, and changeover command means for bringing the
19 decoding control means, the search means and the selected station changeover means into operation.

1 Claim 3 (original): A digital broadcast receiver according to claim 2 wherein the control
2 circuit further comprises register means for storing frequency data as to the other broadcasting
3 stations of satisfactory reception condition searched for by the search means, and the changeover
4 command means reads the frequency data as to one broadcasting station from the register means and
5 operates the selected station changeover means.

1 Claim 4 (original): A digital broadcast receiver according to claim 3 wherein the changeover
2 command means of the control circuit operates the decoding control means and the search means in
3 accordance with a manipulation by the user or when a slightly
4 impaired reception condition is detected by the sensor means to cause the register means to store the
5 result of search.

1 Claim 5 (currently amended): A receiver for receiving a digital broadcast having a
2 transmission spectrum format which is prepared by dividing digital data comprising a single bit
3 stream into a plurality of bit frames capable of holding the same contents as the original bit stream,
4 allocating different carrier frequencies to the respective bit frames and arranging the bit frames on

5 a frequency axis, the digital broadcast receiver comprising a tuner for selecting the desired
6 broadcasting station from among a plurality of broadcasting stations and receiving the digital
7 broadcast of the selected station, a demodulator circuit for decoding a plurality of bit frames
8 constituting the received digital broadcast and outputting the decoded bit frames and a control circuit
9 for controlling the operation of the tuner and the demodulator circuit, the digital broadcast receiver
10 being characterized in that the control circuit comprising:

11 decoding control means for stopping decoding at least one of the bit frames, with one
12 broadcasting station selected,

13 search means for searching for other broadcasting station or stations of satisfactory reception
14 condition utilizing free time resulting from stopping decoding, and

15 selected station changeover means for changing over the broadcasting station being selected
16 for reception to the broadcasting station of satisfactory reception condition searched for, A digital
17 broadcast receiver according to claim 1 wherein the control circuit further comprises memory means
18 for storing received frequency data as to one or a plurality of substitute stations inserted in the
19 broadcast signal being received, and the search means searches for the substitute station of
20 satisfactory reception condition from among said one or plurality of substitute stations having the
21 received frequency data stored in the memory means.

1 Claim 6 (currently amended): A receiver for receiving a digital broadcast having a
2 transmission spectrum format which is prepared by dividing digital data comprising a single bit

3 stream into a plurality of bit frames capable of holding the same contents as the original bit stream,
4 allocating different carrier frequencies to the respective bit frames and arranging the bit frames on
5 a frequency axis, the digital broadcast receiver comprising a tuner for selecting the desired
6 broadcasting station from among a plurality of broadcasting stations and receiving the digital
7 broadcast of the selected station, a demodulator circuit for decoding a plurality of bit frames
8 constituting the received digital broadcast and outputting the decoded bit frames and a control circuit
9 for controlling the operation of the tuner and the demodulator circuit, the digital broadcast receiver
10 being characterized in that the control circuit comprising:

11 decoding control means for stopping decoding at least one of the bit frames, with one
12 broadcasting station selected,

13 search means for searching for other broadcasting station or stations of satisfactory reception
14 condition utilizing free time resulting from stopping decoding, and

15 selected station changeover means for changing over the broadcasting station being selected
16 for reception to the broadcasting station of satisfactory reception condition searched for,

17 ~~A digital broadcast receiver according to claim 1~~ wherein the control circuit further comprises station
18 sensor means for detecting other broadcasting station or stations broadcasting the same program as
19 that being broadcast based on program identifying data inserted in the broadcast signal being
20 received, and memory means for storing received frequency data as to the other broadcasting station
21 or stations detected, and the search means searches for the broadcasting station of satisfactory
22 reception condition from among the broadcasting station or stations having the received frequency

23 data stored in the memory means.

1 Claim 7 (new): A receiver for receiving a digital broadcast having a transmission spectrum
2 format which is prepared by dividing digital data comprising a single bit stream into a plurality of
3 bit frames capable of holding the same contents as the original bit stream, allocating different carrier
4 frequencies to the respective bit frames and arranging the bit frames on a frequency axis, the digital
5 broadcast receiver comprising a tuner for selecting the desired broadcasting station from among a
6 plurality of broadcasting stations and receiving the digital broadcast of the selected station, a
7 demodulator circuit for decoding a plurality of bit frames constituting the received digital broadcast
8 and outputting the decoded bit frames and a control circuit for controlling the operation of the tuner
9 and the demodulator circuit, the digital broadcast receiver being characterized in that the control
10 circuit comprising:

11 decoding control means stopping decoding at least one of the bit frames while not stopping
12 decoding another one of the bit frames, with one broadcasting station selected,

13 search means searching for other broadcasting station or stations of satisfactory reception
14 condition utilizing free time resulting from stopping decoding, and

15 selected station changeover means changing over the broadcasting station being selected for
16 reception to the broadcasting station of satisfactory reception condition searched for.

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